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a gate line on the first substrate;

a metal common line on the first substrate, the common line parallel to the gate line.

a data line on the first substrate, the data line being perpendicular to the gate line;

a common electrode formed of a transparent conductive material on the first substrate;

a thin film transistor having a gate electrode, a source electrode and a drain electrode

formed on the first substrate;

liquid crystal interposed between the first and second substrates; and

a pixel electrode formed of an opaque metal contacting the drain electrode of the thin film transistor, wherein the common electrode is alternating with and being parallel to the pixel electrode.

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the subject application. The Office Action of May 22, 2002 has been received and its contents fully reviewed. By this Amendment, Applicant amends claims 1, 17, 28 and 35, and corrects Figs. 1-8. Claims 1-38 are currently pending in the subject application. Claims 1-38 are rejected by the Office Action dated May 22, 2002. Reexamination and reconsideration of the subject application, as amended, are respectfully requested.

Claims 12-16 are rejected under 35 U.S.C. § 112, first paragraph, as not being enabled. Specifically, the Examiner states that with the recitation of claim 12, "the device of claim 1, further comprising a gate-insulating layer over the pixel electrode" is not illustrated in any of the figures and is not described anywhere in the specification. However, this particular feature is, in fact, described within the specification at least at the paragraphs

spanning pages 13 and 14, lines 22-2. Furthermore, this particular feature is depicted, at least at Fig. 10B.

Accordingly, Applicant respectfully asserts that the subject matter of claim 12 is, in fact, clearly enabled. Furthermore, since claims 13-16 depend from claim 12, and are rejected for the same basis, Applicant respectfully asserts that these claims are adequately enabled.

Accordingly, Applicants respectfully request the withdrawal of the rejection of claims 12-16 under Section 112, first paragraph.

Claims 28-31 are rejected under 35 U.S.C. § 112, second paragraph, as being indefinite. Specifically, claim 28 is rejected for the unclear recitation “a gate pad covering the gate line and the gate pad.” To obviate the rejection, and pursuant to the Examiner’s recommendation, claim 28 has been amended to recite “an auxiliary gate pad covering the gate line and the gate pad,” to more clearly claim Applicant’s invention.

Furthermore, claims 29-31, which depend from claim 28, stand rejected under the same basis, and accordingly with the amendment of claim 28, Applicant respectfully asserts that the rejection of claims 29-31 has been obviated.

Therefore, Applicants respectfully request the withdrawal of the rejection of claims 28-31 under Section 112, second paragraph.

Claims 1, 2, 4, 5, 7, 10, 11, 17, 18 and 33 are rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,362,858 to Jeon et al. (hereinafter the ‘858 patent);

Claims 3, 6, and 34 are rejected under 35 U.S.C. § 103(a) as being unpatentable over the ‘858 patent in view of U.S. Patent No. 6,278,502 to Colgan et al. (hereinafter the ‘502 patent);

Claims 8, 9, and 32 are rejected under 35 U.S.C. § 103(a) as being unpatentable over the '858 patent, as applied to claims 1 and 17 above, and in view of U.S. Patent Application Publication No. 2002/0008824 to Son et al. (hereinafter Pub. No. 824);

Claims 19, 23-25, and 27-30 are rejected under 35 U.S.C. § 103(a) as being unpatentable over the '858 patent, as applied to claims 1 and 17 above, in view of U.S. Patent No. 6,219,125 to Ishikura et al. (hereinafter the '125 patent);

Claim 26 is rejected under 35 U.S.C. § 103(a) as being unpatentable over the '858 patent, in view of U.S. Patent 6,094,250 to Choi et al (hereinafter the '250 patent), as applied to claim 23 above, and further in view of the '502 patent;

Claim 31 is rejected under 35 U.S.C. § 103(a) as being unpatentable over the '858 patent, in view of the '125 patent, as applied to claim 28 above, and further in view of the '502 patent;

Claims 20, 21, and 22 are rejected under 35 U.S.C. § 103(a) as being unpatentable over the '858 patent, as applied to claims 1 and 17 above, in view of the '250 patent;

Claims 35-38 are rejected under 35 U.S.C. § 103(a) as being unpatentable over the '858 patent, in view of U.S. Patent No. 6,356,328 to Shin et al. (hereinafter the '328 patent); and

Claims 36-37 are rejected under 35 U.S.C. § 103(a) as being unpatentable over the '858 patent, in view of the '328 patent, as applied to claim 35 above, and further in view of U.S. Patent No. 6,049,365 to Nakashima (hereinafter the '365 patent).

Independent claims 1, 17, and 35, and similarly claims 2-16, 18-34 and 36-38, which depend therefrom, are allowable over the cited references in that each of these claims recites a combination of elements, including, for example, the common electrode is alternating with

and being parallel to the pixel electrode. None of the cited references, singly or in combination, teaches or suggests at least these features of the claimed invention.

Accordingly, Applicant respectfully submits that Independent Claims 1, 17, and 35, and dependent claims 2-16, 18-34, and 36-38 which depend therefrom, are allowable over the cited references. Applicant believes the foregoing amendments place the Application in condition for allowance and early and favorable action is respectfully solicited.

Should the Examiner deem that a telephone conference would further the prosecution of this Application, the Examiner is invited to call the other side at (202) 496-7500. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the U.S. Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, any additional fees required under 37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete this filing of this response, may be charged to Deposit Account No. 50-0911.

Please credit any overpayment to Deposit Account No. 50-0911.

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Respectfully submitted,



By Song K. Jung

Song K. Jung
Registration No.: 35,210

Rebecca G. Rudich

Registration No.: 41,786

William D. Titcomb

Registration No.: 46,463

MCKENNA LONG & ALDRIDGE LLP
1900 K Street, N.W.
Washington, DC 20006
(202) 496-7500
Attorneys for Applicant

Attachment: Version with Marking to Show Changes
Request for Approval of Drawing Changes

VERSION WITH MARKINGS TO SHOW CHANGES MADE

1. An in-plane switching liquid crystal display device comprising:
 - a gate line on a first substrate;
 - a data line on the first substrate, the data line being perpendicular to the gate line;
 - a common line on the first substrate, the common line being parallel with the gate line and being formed of a metal;
 - a pixel electrode and a common electrode on the first substrate, the pixel and common electrodes being formed of a transparent conductive material; and
 - a liquid crystal layer between the first and second substrates, wherein the common electrode is alternating with and being parallel to the pixel electrode.
17. An in-plane switching Liquid Crystal Display (LCD) device, comprising:
 - a first substrate and a second substrate;
 - a gate line on the first substrate;
 - a metal common line on the first substrate, the common line parallel with the gate line.
 - a data line on the first substrate, the data line being perpendicular to the gate line;
 - a common electrode on the first substrate;
 - a thin film transistor having a gate electrode, a source electrode and a drain electrode formed on the first substrate;
 - liquid crystal interposed between the first and second substrates;
 - a pixel electrode contacting the drain electrode of the thin film transistor; and

wherein, the pixel and common electrodes are formed of a transparent conductive material and the common electrode is alternating with and being parallel to the pixel electrode.

28. The LCD device of claim 17, further comprising an auxiliary gate line and a an auxiliary gate pad covering the gate line and the gate pad.
35. An in-plane switching Liquid Crystal Display (LCD) device, comprising:
 - a first substrate and a second substrate
 - a gate line on the first substrate;
 - a metal common line on the first substrate, the common line parallel to the gate line.
 - a data line on the first substrate, the data line being perpendicular to the gate line;
 - a common electrode formed of a transparent conductive material on the first substrate;
 - a thin film transistor having a gate electrode, a source electrode and a drain electrode formed on the first substrate;
 - liquid crystal interposed between the first and second substrates; and
 - a pixel electrode formed of an opaque metal contacting the drain electrode of the thin film transistor, wherein the common electrode is alternating with and being parallel to the pixel electrode.